The NCPCI-EXT64 is a passive 64 bit CompactPCI extender board intended for use with CompactPCI boards for testing and debug purposes. It eases debugging of CompactPCI boards by enabling the user to access the board under test from both sides as well as to install debug port cables. For measurement purposes the NCPCI-EXT64 allows access to all PCI, I/O and H.110 bus signals.

The NCPCI-EXT64 is ideally suited for debugging and testing CompactPCI cards with or without H.110 bus in a CompactPCI chassis.
The NCPCI-EXT64 is a passive 64-bit CompactPCI extender board, i.e. it does not contain any circuitry but two CompactPCI connector sets. One is used to plug into the backplane of the chassis and the second is dedicated to receive the board under test.

The NCPCI-EXT64 provides several male header connectors carrying all PCI bus signals, I/O signals, and H.110 specific signals routed between the two sets of CompactPCI connectors for access by measuring equipment, i.e. they do directly fit the probes of a Tektronix TLA logic analyzer.

The signals on these male header connectors are grouped by eight. One row of these double-row header connectors carries the signals, the other is grounded.

The board also provides several LEDs and test points for the power supply lines and ground.

All power supply lines may be disrupted by opening wire bridges for current measurements.

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**CompactPCI Interface**
33/66MHz, 64-bit

**Power Consumption**
Current drawn from +3.3V, +5V, +12V, -12V, V(I/O) : less than 10mA each.
+3.3V, +5V, V(I/O), and GND connected to separate power planes

All power supply lines drive signaling LEDs. All power supply lines may be cut by opening wire bridges for current measurements.

**Environmental**
-40°C to +85°C (operating and storage)
Relative Humidity: 5% to 90% (non-condensing)

**Standard Compliance**
PCI Rev. 2.4a
PICMG2.1 R2.0
PICMG 2.5 R1.0